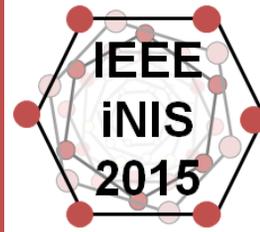


# 1<sup>st</sup> IEEE INTERNATIONAL SYMPOSIUM ON NANOELECTRONIC and INFORMATION SYSTEMS (DEC 21-23, 2015 @ INDORE, INDIA)

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The **primary objective of iNIS** is to provide a platform for both hardware and software researchers to interact under one umbrella for further development of efficient and secure information processing technologies. Efficient and secure data sensing, storage and processing play pivotal roles in the current information age. The state-of-the-art nanoelectronic technology based hardware systems caters to the needs of efficient sensing, storage, and computing. At the same time, efficient algorithms and software used for faster analysis and retrieval of desired information are becoming increasingly important. Big data which are large, complex data sets, are now a part of the Internet world. Storing and processing needs of the enormous amount of structured and unstructured data are getting increased challenging. At the same time, Internet of Things (IoT) and cyber-physical systems (CPS) have been evolving with the simultaneous development of hardware and software. The performance and efficiency of the present as well as the future generation of computing and information processing systems are largely dependent upon advances in both hardware and software.

## CONTRIBUTIONS ARE SOUGHT IN THE FOLLOWING AREAS:

- Nanoelectronic VLSI and Sensor Systems (NVS)
- Energy-Efficient, Reliable VLSI Systems (ERS)
- Hardware/Software Solutions for Big Data (SBD)
- Hardware/Software for Internet of Things (IOT)
- Hardware for Secure Information Processing (SIP)
- Cyber Physical Systems and Social Networks (CSN)

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**Acceptance Notification:** September 14, 2015

**Submission of Final Version:** October 12, 2015

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